

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Syouji NOGAMI et al.

Group Art Unit : 2813

Appl. No. : 10/562,235

Examiner : Sonya D Mccall Shepard

Filed : October 31, 2006

Confirmation No. : 8539

For : MANUFACTURING METHOD OF SEMICONDUCTOR WAFER AND  
SEMICONDUCTOR WAFER MANUFACTURED BY THIS METHOD

**COVER LETTER REGARDING REPLACEMENT SHEETS**

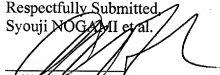
Commissioner for Patents  
U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Sir :

Enclosed are two replacement sheets of formal drawings including the revisions made as agreed upon and approved by the Examiner in the Examiner's Amendment included in the Notice of Allowability dated May 10, 2010. The term "speed" is replaced by the term "rate" in fig. 6 and 8.


The U.S. Patent and Trademark Office is hereby authorized to charge any additional fees concerning the application to Deposit Account No. 19-0089.

Respectfully Submitted,  
Syouji NOGAMI et al.

  
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June 24, 2010  
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